

The listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1.-9. (Canceled)

10. (Currently Amended) The demodulator according to claim ~~[[9]]~~ 11, wherein said decoding is Viterbi decoding.

11. (Currently Amended) ~~The demodulator according to claim 9,~~ A demodulator in a digital broadcasting receiver for receiving a digital broadcast by layered transmission system, said demodulator comprising:

pseudo-data inserting means for inserting pseudo-data for decoding a transmission and multiplexing configuration control (TMCC) signal before and/or after said TMCC signal;

wherein said pseudo-data inserting means comprises a serial/parallel converter for serial/parallel converting a demodulated base band signal, a pseudo-data generator for generating said pseudo-data, and a selector for selecting output data of said serial/parallel converter and said pseudo-data outputted from said pseudo-data generator,

wherein said pseudo-data at least includes related fixed data maintaining a convoluted time sequential relationship.

12. (Currently Amended) ~~The demodulator according to claim 9,~~ A demodulator in a digital broadcasting receiver for receiving a digital broadcast by layered transmission system, said demodulator comprising:

pseudo-data inserting means for inserting pseudo-data for decoding a transmission and multiplexing configuration control (TMCC) signal before and/or after said TMCC signal;

wherein said pseudo-data inserting means comprises a serial/parallel converter for serial/parallel converting a demodulated base band signal, a pseudo-data generator for generating said pseudo-data, and a selector for selecting output data of said serial/parallel converter and said pseudo-data outputted from said pseudo-data generator,

wherein said pseudo-data ~~further~~ includes fixed data and a synchronization word for frame identification.

13. (Currently Amended) The demodulator according to claim [[9]] 11, further comprising:

an accumulator to which said output data of said serial/parallel converter are written at a half of a transmission rate and from which the written data are read out at the same rate as said transmission rate to be outputted to said selector.

14. (Currently Amended) A method of demodulation for use in a digital broadcasting receiver for receiving a digital broadcast by layered transmission system, said method comprising [[a]] the step of inserting pseudo-data for decoding a transmission and multiplexing configuration control (TMCC) signal before and/or after said TMCC signal, wherein said inserting step comprises the steps of serial/parallel converting a base band signal demodulated by a demodulation step, generating said pseudo-data, and selecting output data of said serial/parallel converting step and said pseudo-data outputted from said pseudo-data generating step,

wherein said pseudo-data at least includes related fixed data maintaining a convoluted time sequential relationship.

15. (New) The demodulator according to claim 12, wherein said decoding is Viterbi decoding.

16. (New) The demodulator according to claim 12, further comprising:  
an accumulator to which said output data of said serial/parallel converter are written at a half of a transmission rate and from which the written data are read out at the same rate as said transmission rate to be outputted to said selector.